Micronet news

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Micronet Annual Workshop

- Micronet's Annual Workshop was held on April 26th and 27th, 1999 at the Westin Hotel in Ottawa. Over 160 people attended from university, industry and government organizations from across the country.

- The PLENARY SESSION on April 26th included two invited speakers. Professor David Hodges' presentation dealt with *"University-Industry Interaction and Intellectual Property"*. Professor Asad Abidi's presentation dealt with *"Wireless Transceivers Integrated in CMOS Technology"*.

- ORAL and POSTER PRESENTATIONS featured highlights from Micronet's projects in the Circuits, Devices and Systems areas.

- The luncheon on April 26th was sponsored by Gennum Corporation. Doug Barber, President and CEO of Gennum, expressed his appreciation for the research community's contributions to the company's success over its twenty-five year history. The keynote presentation by Keith Parsonage, Director General, Information and Communications Technologies Branch of Industry Canada, discussed Canada's effort to attract major investment into semiconductor manufacturing.

- The status of the network was discussed during Micronet's ANNUAL GENERAL MEETING.

- The dinner that followed featured Denzil Doyle, who discussed the Business Opportunity Document, a useful tool for packaging technology exploitation opportunities in a format that is more visible and understandable by potential exploiters.

- On April 27th, the LONG TERM VISION for the Network was the topic of a panel discussion and included presentations by members of the Coordinating Committee.

- Six Industrial Partners held a JOB FAIR on April 27th. Participating companies included:

  - Gennum
  - Goal Electronics
  - Mitel Semiconductor

  - MOSAID
  - Nortel Networks
  - PMC-Sierra

- The recipients of this year's BEST PAPER AWARD were:

  DEVICES   J. Rogers, Carleton University, for the presentation *"Cu Inductors for RF Applications: A Post-Processing Approach"*.
  S. Delprat, INRS, for the presentation *"Optimization of Deposition and Etching Processes for the Development of BST/Pt Devices"*.

  CIRCUITS   H. Djahanshahi, University of Toronto, for the presentation *"Differential CMOS Circuits for High Speed Monolithic Clock and Data Recovery Applications"*.
  D. Suvakovic, University of Toronto, for the presentation *"Arithmetic Circuits for Adiabatic DSP Systems"*.

  SYSTEMS   S. Sadeghi-Emamchaie, University of Windsor, for the presentation *"Cellular Neural Networks for Low Noise Digital Arithmetic Arrays"*.

  The selection committee for the Best Paper Award consisted of:

  - P. DeMone, MOSAID
  - D. Lynch, Gennum Corporation
  - T. MacElwee, Nortel Networks

  - J. Molnar, Mitel Semiconductor
  - M. Bitar (secretary, non-voting), Micronet